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APPLICATION NO.	FILI	NG DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
09/943,778	08/	30/2001	Kunal R. Parckh	4475.1US (98-1097.1)	2586
24247	7590	11/24/2003		EXAMINER	
TRASK BE				PHAM, I	IOAI V
P.O. BOX 2 SALT LAKI		84110		ART UNIT PAPER NUM 2814	
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DATE MAILED: 11/24/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

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	Application No.	Applicant(s)					
Office Action Summan	09/943,778	PAREKH ET AL.	PAREKH ET AL.				
Office Action Summary	Examiner	Art Unit					
The MAN INO DATE of this committee is	Hoai V Pham	2814					
The MAILING DATE of this communication app Period for Reply	ears on the covers	ineet with the correspondence a	aaress				
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply if NO period for reply is specified above, the maximum statutory period with the period for reply within the set or extended period for reply will, by statute, - Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b). Status	36(a). In no event, however within the statutory minim will apply and will expire SI cause the application to b	er, may a reply be timely filed num of thirty (30) days will be considered tim X (6) MONTHS from the mailing date of this become ABANDONED (35 U.S.C. § 133).					
1) Responsive to communication(s) filed on <u>06 Ai</u>	<u>ugust 2003</u> .						
2a) ☐ This action is FINAL . 2b) ☑ This	action is non-final.						
3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.							
Disposition of Claims							
 4) Claim(s) 1,4-10 and 13-24 is/are pending in the application. 4a) Of the above claim(s) is/are withdrawn from consideration. 5) Claim(s) is/are allowed. 6) Claim(s) 1,4-10 and 13-24 is/are rejected. 7) Claim(s) is/are objected to. 8) Claim(s) are subject to restriction and/or election requirement. 							
Application Papers							
9) The specification is objected to by the Examine 10) The drawing(s) filed on is/are: a) access applicant may not request that any objection to the Replacement drawing sheet(s) including the correct 11) The oath or declaration is objected to by the Exemption Priority under 35 U.S.C. §§ 119 and 120	epted or b) obje drawing(s) be held ir ion is required if the	n abeyance. See 37 CFR 1.85(a). drawing(s) is objected to. See 37 (
12)Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).							
a) All b) Some * c) None of: 1. Certified copies of the priority documents 2. Certified copies of the priority documents 3. Copies of the certified copies of the priority application from the International Bureau * See the attached detailed Office action for a list 13) Acknowledgment is made of a claim for domestic since a specific reference was included in the first 37 CFR 1.78. a) The translation of the foreign language pro 14) Acknowledgment is made of a claim for domestic reference was included in the first sentence of the	s have been received the second of the certified coper priority under 35 st sentence of the servisional application of priority under 35 or priority under 35 or priority under 35 or priority under 35	red in Application No re been received in this National). siles not received. U.S.C. § 119(e) (to a provision specification or in an Application has been received. U.S.C. §§ 120 and/or 121 since	al application) n Data Sheet. e a specific				
Attachment(s)							
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449) Paper No(s)	5) 🔲 N	nterview Summary (PTO-413) Paper No Notice of Informal Patent Application (P Other:					

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DETAILED ACTION

Claim Rejections - 35 USC § 103

- 1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 2. This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).
- 3. Claims 1,4, 6-9, 10, 13-15, 17-24 are rejected under 35 U.S.C. 103(a) as being unpatentable over Prall et al. [U.S. Pat. 6,274,423] previously applied, in view of Sandhu et al. [U.S. Pat. 6,124,626] previously applied and Derderian et al. [U.S. Pat. 6,399,982] newly cited.

With respect to claims 1, 9-10, 20-21 and 23, Prall et al. (figures 12-20, cols. 1-8) discloses a DRAM comprising:

a semiconductor substrate (12) having an active region thereon and a capacitor structure disposed above the active region, the capacitor structure including a storage node (42), a dielectric layer (44) overlying the storage node, and a conductive cell plate (46) overlying the dielectric layer, each of the dielectric layer and the conductive cell plate having an end portion proximate a conductive contact (60), the conductive contact extending downward and adjacently beside the capacitor structure, the end portion of the dielectric layer extending closer to the conductive contact than the end portion of the storage node and the conductive cell plate (see figure 20); and

a doped BPSG layer (56) disposed over the capacitor structure and encasing the end portions of the dielectric layer and the conductive cell plate, the BPSG layer disposed between the capacitor structure and the conductive contact, the conductive contact extending through the BPSG layer (see figure 20).

Prall et al. does not explicitly teach: a) a second TEOS layer disposed between the BPSG layer and the capacitor structure. b) a first TEOS layer disposed proximate the storage node.

Regard to a), Sandhu et al. discloses that the second TEOS layer (57) disposed between the BPSG layer (53) (col. 3, lines 30-34) and the capacitor structure (48, 50, 52) to wrap around difficult edges or plates (see figures 1-4, col. 6, lines 43-60). Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to use the TEOS layer disposed between the BPSG layer and the capacitor structure as taught by Sandhu et al. in the device of Prall et al. in order to

wrap around difficult edges, plates and provide dielectric oxygen loss protection (col. 6, lines 47-50).

Regard to a), Derderian et al. discloses that the first TEOS layer (120) disposed proximate the storage node (126) (see figures 4-5, col. 4, lines 6-20). Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to use the TEOS layer disposed proximate the storage node as taught by Derderian et al. in the device of Prall et al. in order to prevent dopant migration (col. 4, lines 17-20).

As being mention above, it would have been obvious to one having ordinary skill in the art at the time the invention was made to modify the device of Prall et al. by using the TEOS layer disposed between the BPSG layer and the capacitor structure as taught by Sandhu et al. and the TEOS layer disposed proximate the storage node as taught by Derderian et al. as being claimed to wrap around difficult edges, plates and prevent dopant migration into the active region.

With respect to claims 4 and 15, Prall et al. discloses that the storage node and the conductive cell plate are heavily doped with dopants (see col. 4, lines 30-32 and lines 42-45).

With respect to claims 6, 7, 17 and 18, Prall et al. discloses that the dielectric layer comprises a capacitor cell dielectric (nitride) layer (see col. 4, lines 41-42).

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With respect to claims 8 and 19, Prall et al. discloses that the capacitor structure comprises a container-shaped capacitor (see figure 20).

With respect to claims 9 and 20, Sandhu et al. discloses that the TEOS layer (57) is a dopant barrier (col. 4, lines 54-57) between the capacitor structure and the BPSG (53) (see figure 2).

With respect to claim 13 Prall et al. discloses that the conductive contact comprises at least one of metal (see col. 5, lines 65-67).

With respect to claim 14 Prall et al. discloses that the conductive contact comprises a digit line (62) (see figure 20).

With respect to claims 22 and 24, Prall et al. does not explicitly disclose the thickness of the TEOS layer is about 100 Angstroms and 250 Angstroms. However, the thickness range would have been obvious to an ordinary artisan practicing the invention because, absent evidence of disclosure of criticality for the range giving unexpected results, it is not inventive to discover optimal or workable ranges by routine experimentation. In re Aller, 220 F.2d 454, 105 USPQ 233, 235 (CCPA 1955). Furthermore, the specification contains no disclosure of either the critical nature of the claimed dimensions of any unexpected results arising therefrom. Where patentability is aid to be based upon particular chosen dimensions or upon another variable recited in a claim, the Applicant must show that the chosen dimensions are critical. See In re Woodruff, 919 F.2d 1575, 1578, 16 USPQ2d 1934, 1936 (Fed. Cir. 1990).

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4. Claims 5 and 16 are rejected under 35 U.S.C. 103(a) as being unpatentable over Prall et al. [U.S. Pat. 6,274,423] previously applied, Sandhu et al. [U.S. Pat. 6,124,626] previously applied, and Derderian et al. [U.S. Pat. 6,399,982] newly cited as applied to claims 1-4, 6-9, 10-15 and 17-20 above, and further in view of Tsai [U.S. Pat. 5,763,306] previously applied.

Prall et al., Sandhu et al. and Derderian et al. substantially disclose the claimed of the DRAM device as discussed in details above except that the storage node and the conductive cell plate are doped with phosphorous. However, Tsai shows that the storage node and the conductive cell plate are doped with phosphorous to increase conductivity (see col. 6, lines 12-22). Therefore, it would have been obvious to skilled in the art to dope phosphorous in the storage node and the conductive cell plate as taught by Tsai in the device of the above combination in order to increase conductivity of the storage node and the conductive cell plate thus increase the capacitance of the capacitor.

Response to Arguments

5. Applicant's arguments with respect to claims 1, 4-10, and 13-20 have been considered but are most in view of the new ground(s) of rejection.

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Conclusion

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Hoai V Pham whose telephone number is 703-308-6173. The examiner can normally be reached on 7:30A.M. - 6:00P.M..

- 7. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wael M. Fahmy can be reached on 703-308-4918. The fax phone number for the organization where this application or proceeding is assigned is 703-308-7722.
- 8. Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-0956.

Hoai Pham

November 15, 2003

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